Amendments to the Specification

Please amend paragraph [0007] as follows:

If the control loop bandwidth of the APC control loop is <u>too wide</u> ~~to wide~~, i.e., the loop responds quickly, it may track variations in output power caused by the data pattern. This introduces pattern-dependent jitter into the bias current control and is to be avoided. Typically, a narrow APC control loop bandwidth, i.e., a loop that responds slowly, is desired so that the bias current value is adjusted in a manner to maintain the desired average output power level.

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